



## Interns Wanted!

Rambus, a premier chip and silicon IP provider, is seeking to hire an exceptional Software Engineer Intern to join our Rambus PCIe Controller Group team in Aix en Provence. Candidates will be joining some of the brightest inventors and engineers in the world to develop products that make data faster and safer.

As a Software Engineer Intern, the candidate will be reporting to the Internship Supervisor and is a Part Time position as intern. You will develop the XpressAgent SDK, a Python API to control and debug internal signals on PCIe and CXL Rambus IPs. You will work on the software architecture, the definition and the implementation of the Core and product-specific services. Finally you will be in charge of the development of the associated User Interface.

Rambus offers a flexible work environment, embracing a hybrid approach for the majority of our office-based roles. We encourage employees to spend an average of at least three days per week working onsite, allowing for two days of remote work.

### **Responsibilities:**

Master thesis project completion:

- o Study the existing proof of concept.
- o Define the requested services to implement and the functional interfaces to support.

- o Define the project and the software architectures.
- o Implementation, documentation, and validation.
- o Results evaluation
- o Master Thesis Report redaction

### **Requirements/Qualifications:**

Last year of a Master Degree program in Software Engineering, or equivalent

#### . Software development:

- o Python 3
- o Low level programming, [UART protocol](#)
- o UI/UX

#### . Software architecture analysis

### **About Rambus**

Rambus is a global company that makes industry-leading memory interface chips and Silicon IP to advance data center connectivity and solve the bottleneck between memory and processing. With over 30 years of semiconductor experience, we are a leading provider of high-performance products and innovations that maximize the bandwidth, capacity and security for AI and other data-intensive workloads. Our world-class team is the foundation of our company, and our innovative spirit drives us to develop the cutting-edge products and technologies essential for tomorrow's systems.

Rambus is committed to cultivating a culture where we actively seek to understand, respect, and celebrate the complex and rich identities of ourselves and others. Our Diversity, Equity, and Inclusion initiatives are geared towards valuing the differences in backgrounds, experiences, and thoughts at Rambus to help enhance collaboration, teamwork, engagement, and innovation. At Rambus, we believe that we can be our best when every member of our organization feels respected, included, and heard.

Rambus is proud to be an Equal Employment Opportunity and Affirmative Action employer. We do not discriminate based upon race, religion, color, national origin, sex (including pregnancy, childbirth, or related medical conditions), sexual orientation, gender identity, gender expression, age, status as a protected veteran, status as an individual with a disability, genetic information, or other applicable legally protected characteristics.

Rambus is committed to providing reasonable accommodations for qualified individuals with disabilities and disabled veterans during our job application procedures. If you require assistance or an accommodation due to a disability, please feel free to inform us in your application.

Rambus does not accept unsolicited resumes from headhunters, recruitment agencies or fee-based recruitment services.

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## Interns Wanted!

Rambus, a premier chip and silicon IP provider, is seeking to hire an exceptional RTL Design Engineer Intern to join our Rambus PCIe Controller Group team in Aix en Provence. Candidates will be joining some of the brightest inventors and engineers in the world to develop products that make data faster and safer.

As a Design Engineer Intern, the candidate will be reporting to the Internship Supervisor and is a Part Time position as intern. You will integrate Rambus design team in France and will be responsible for developing and validating a bridge from the Arm AXI protocol to the CXL CPI protocol. You will also integrate this design in specific Rambus test environments and validate the solution in FPGA.

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### **Responsibilities:**

Master thesis project completion:

- Study of AMBA and CXL CPI protocols
- Study and discovery of Rambus development environment



- Definition and proposition of a hardware architecture
- Implementation in Verilog
- Functional verification using QuestaSim
- FPGA validation of the solution using Xilinx VUSP board
- Documentation
- Master Thesis Report redaction

### **Requirements/Qualifications:**

- Last year of a master's degree program in Electronic Engineering, or equivalent
- RTL development:
  - Design using Verilog and System Verilog languages
  - Logic Simulation
  - Logic FPGA Synthesis
- Python and other scripting languages and tools

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As a Software Engineer Intern, the candidate will be reporting to the Internship Supervisor and is a Part Time position as intern. You will develop the XpressAgent SDK, a Python API to control and debug internal signals on PCIe and CXL Rambus IPs. You will develop a fully new version of the Inspector Software, a tool to interact and monitor a PCIe link. You will work on the development of the software interface with the Interposer board, and on the conception of the User Interface.

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Master thesis project completion:

- o Study the existing proof of concept.

- o Define the requested services to implement and the functional interfaces to support.
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- o Implementation, documentation, and validation.
- o Results evaluation
- o Master Thesis Report redaction

### **Requirements/Qualifications:**

Last year of a Master Degree program in Software Engineering, or equivalent

. Software development:

- o Python 3
- o UI/UX and Front-end development
- o C#

. Software architecture analysis

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